

Interference Search History Printout MT

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(memory and trench and trench near capacitor and access near transistor and contact near region and channel near region and control near electrode).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/10/03 10:52